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Notice of Allowability	Application No.	Applicant(s)	
	10/580,466	KAWAMURA ET AL.	
	Examiner	Art Unit	
	Tuyen Kim Vo	2876	
	Tuyen Kiin vo	2070	
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comn IGHTS. This application is	in this application. If not included nunication will be mailed in due cou	urse. THIS
1. This communication is responsive to <u>09/14/2007</u> .			
2. The allowed claim(s) is/are <u>1-12</u> .			
3. Acknowledgment is made of a claim for foreign priority ur	nder 35 U.S.C. § 119(a)-(d)	or (f).	
a) ☐ All b) ☐ Some* c) ☐ None of the:			
1. Certified copies of the priority documents have	e been received.	•	
2. Certified copies of the priority documents have	e been received in Applicat	ion No	
3. Copies of the certified copies of the priority do	cuments have been receive	ed in this national stage applicatior	n from the
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	MENT of this application.		·
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			ICE OF
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.		
(a) I including changes required by the Notice of Draftspers	son's Patent Drawing Revie	ew (PTO-948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date	<i>.</i>		
(b) including changes required by the attached Examiner' Paper No./Mail Date	s Amendment / Comment o	or in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on he header according to 37 C	the drawings in the front (not the baFR 1.121(d).	ck) of
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			e the
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Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5 🗆 Notice of I	nformal Patent Application	
2. Notice of Praftperson's Patent Drawing Review (PTO-948)		Summary (PTO-413),	
	Paper No	./Mail Date	
3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date	7. ⊠ Examiner'	s Amendment/Comment	
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛭 Examiner'	s Statement of Reasons for Allowa	ince
	9.	·	
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DETAILED ACTION

Acknowledgment

1. This Office Action is responsive to the Amendment filed on 09/14/2007.

Examiner's Amendment

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

The recitation of "is capable of" in lines 2 and 6 of The abstract is changed to - - executes - -.

Reasons for Allowance

3. The following is an examiner's statement of reasons for allowance: The prior art of record, taken alone or in combination, fails to teach or fairly suggest the arrangement of a semiconductor memory which is capable of executing at least two application programs that comprising a first storage unit operable to store data relating to a first application program; a second storage unit operable to store data relating to a second application program; and especially, a level identification unit operable to identify respective security levels of the first and second application programs for the data relating to the first and second application programs for identifying

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a security level of an application program for data relating to the application program; and a duplication unit operable to duplicate the data stored in the first storage unit and to store the duplicated data into the second storage unit, without taking the data outside of the semiconductor memory, in the case where a relationship between the two security levels identified by the level identification unit meets a predetermined condition as recited in claims 1 (and its dependents) and 9-12.

The previous cited references Hosogoe et al. (US 6,766,961 B2), Taylor (US 5,578,808), Gochi (5,055,661) teach an integrated circuit (IC) card that having a plurality of memory units operable to store different application data.

The previous cited references Johnson et al. (US 5,813,009), Bradley et al. (US 6,957,256 B1) and Yakir et al. (US 2004/0049513 A1) teach a data management system with security level access.

However, without the benefit of applicant's teaching, there is no motivation for one of ordinary skill in the art at the time the invention was made to selectively combine the features of the prior art in a manner so as to create the claimed invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuyen Kim Vo whose telephone number is 571-270-1657. The examiner can normally be reached on Monday - Friday, 7:30a.m. - 5:00p.m., EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael G. Lee can be reached on 571-272-2398. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Tuyen Kim Vo Patent Examiner Art Unit 2876 October 31, 2007.

SUPERVISORY PATENT EXAMINER

PECHNOLOGY CENTER 2800